

ECE124a

University of California, Santa Barbara
Department of Electrical and Computer Engineering
Fall 2002

- Class Room:** Brda 2019B Tu-Th 3:30-4:45
- Labs:** Monday 6:00-8:50 Engr1 ECI (TBA)
Friday 12:00-3:00 Engr1 ECI (TBA)
- Instructor:** Forrest Brewer, Engr 1, 4159 x8043
- Office Hours:** Tu-W 1:30-2:30PM, or by appt.
- TA:** Chao Huang chao@engineering.ucsb.edu
Radha Kanekal rkanekal@yahoo.com
- Text:** Modern VLSI Design System-on-Chip Design, Wayne Wolf (third edition) Prentice_Hall, 2002 (Readings and Homework will be assigned from this book).
ECE124a Slides Forrest Brewer
(Available at the Alternative Copy Shop)
- References:** Analysis and Design of Digital Integrated Circuits, Hodges and Jackson (second edition) Mc Graw Hill, 1988. (RBR)
Quiz Keys, Homework Solutions and old Final Key are available on Web:
<http://bears.ece.ucsb.edu/class/ece124a>.
- Grading:** 7 Weekly Exams (drop low score) + Final (total 50%)
HW + Labs + Project (total 50%)
- Notes:** Alternative copy shop or RBR for foils (reader) used in class. Homework will be used as evidence of effort in borderline grading cases. Homework will be graded and returned for personal study. Weekly exams on each Thursday after lecture, 20 minutes. Project must be completed to pass.